

# Interconnect Fabric Module

00

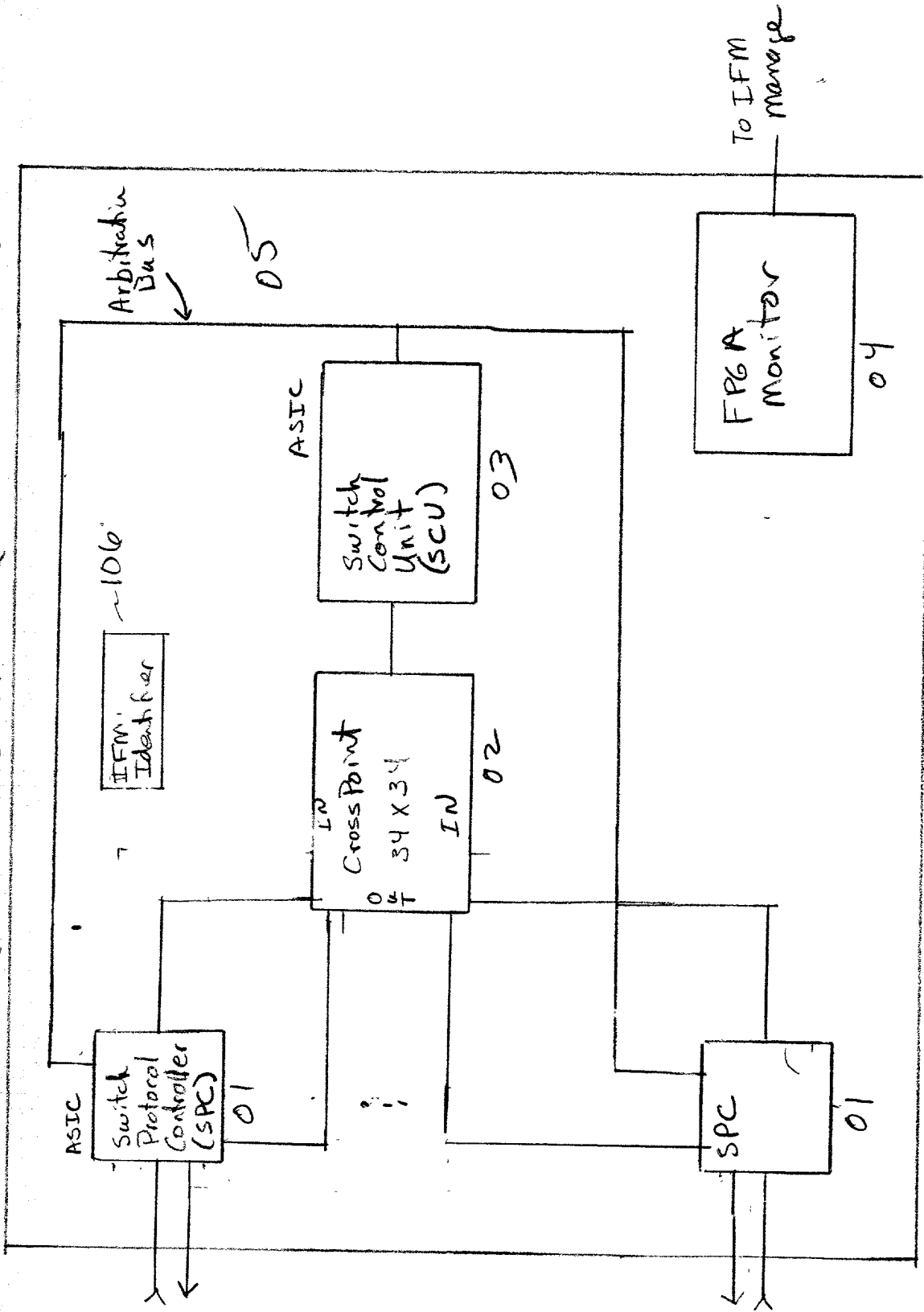


Fig 1

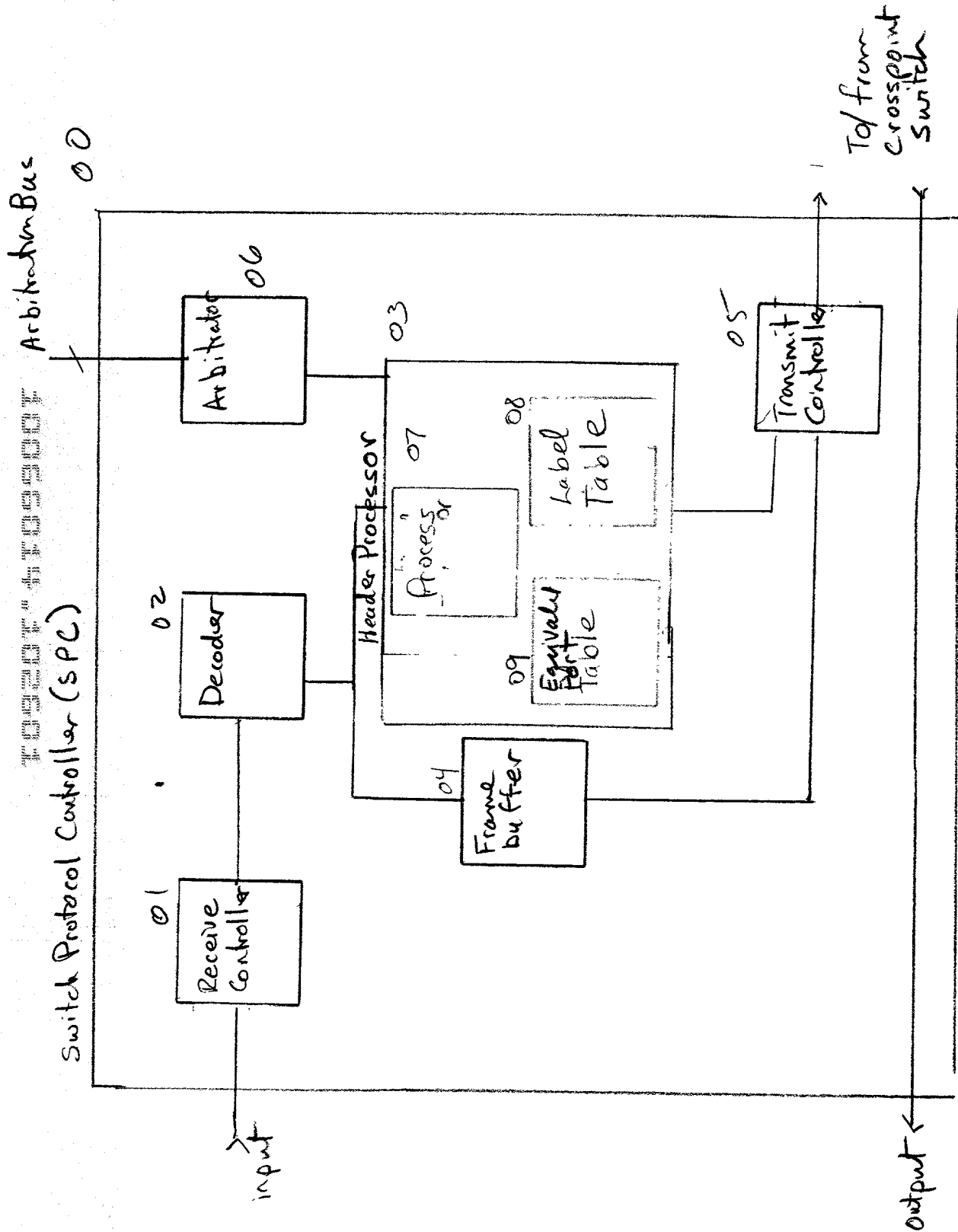


Fig 2

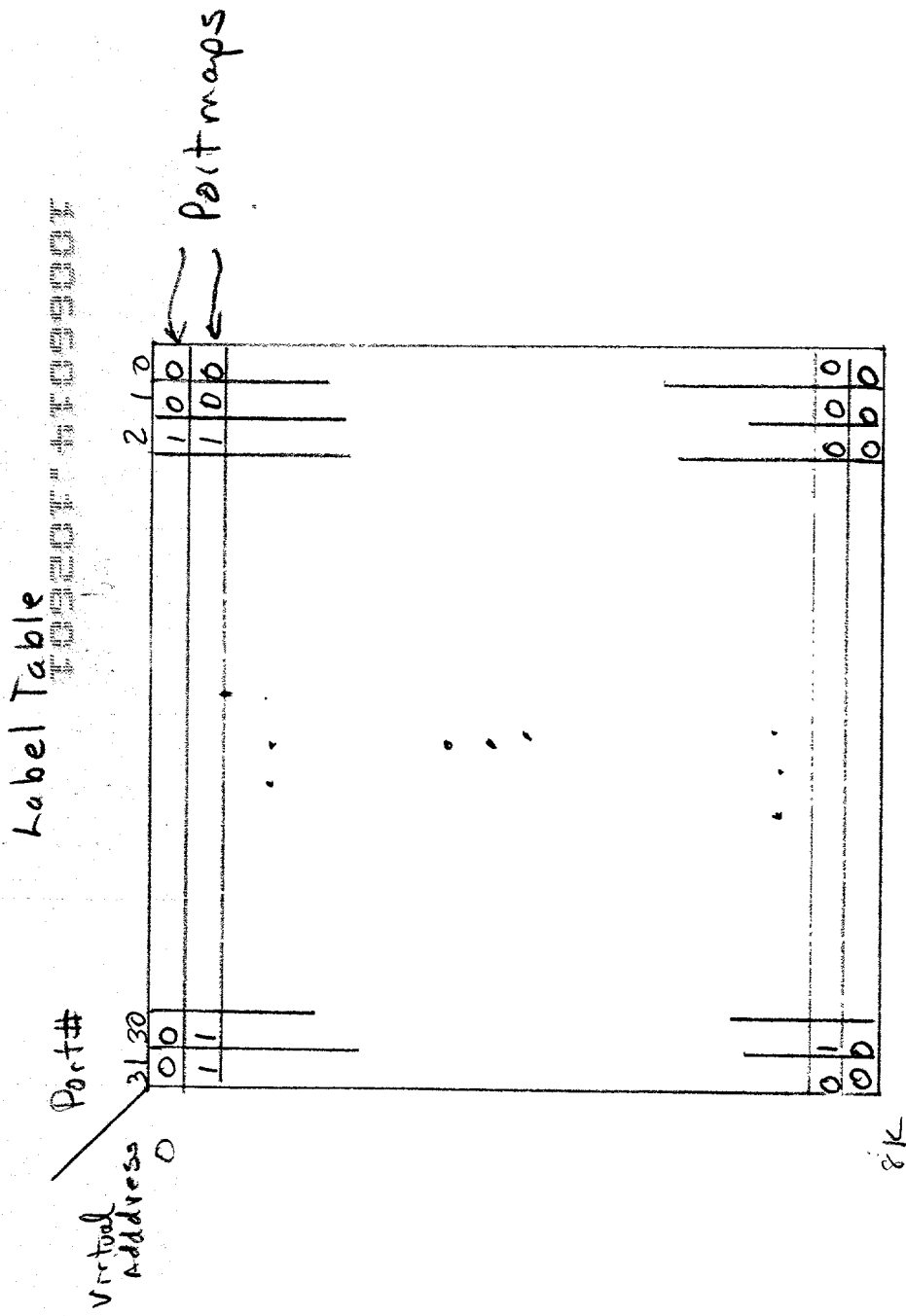


Fig 3

# Frame

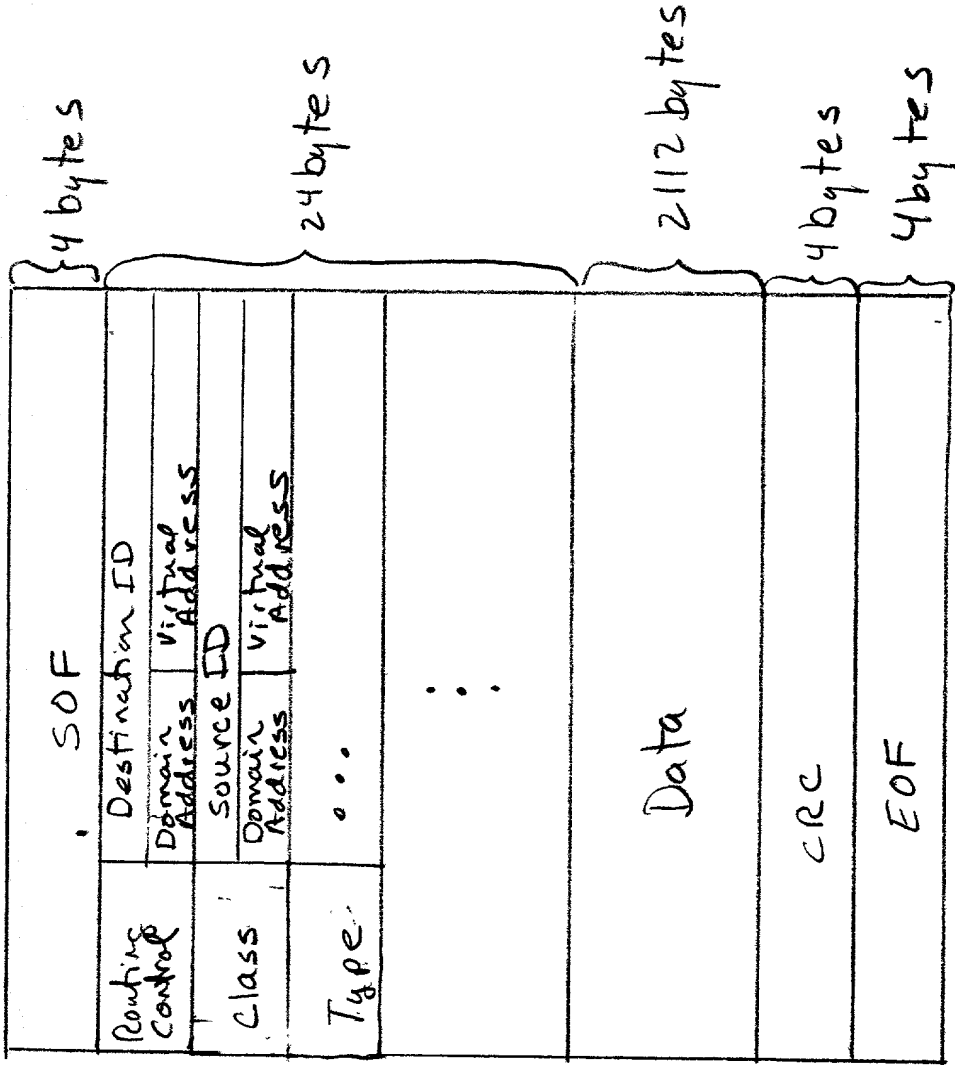


Fig 4

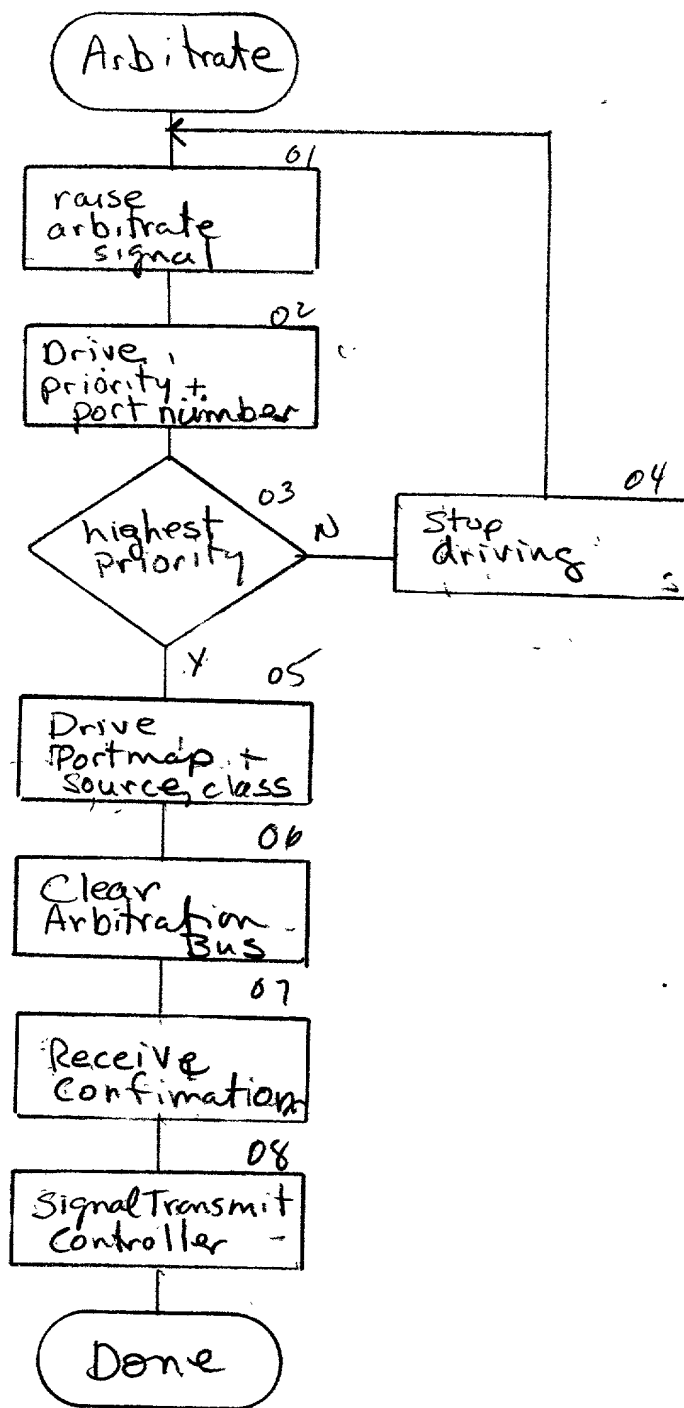
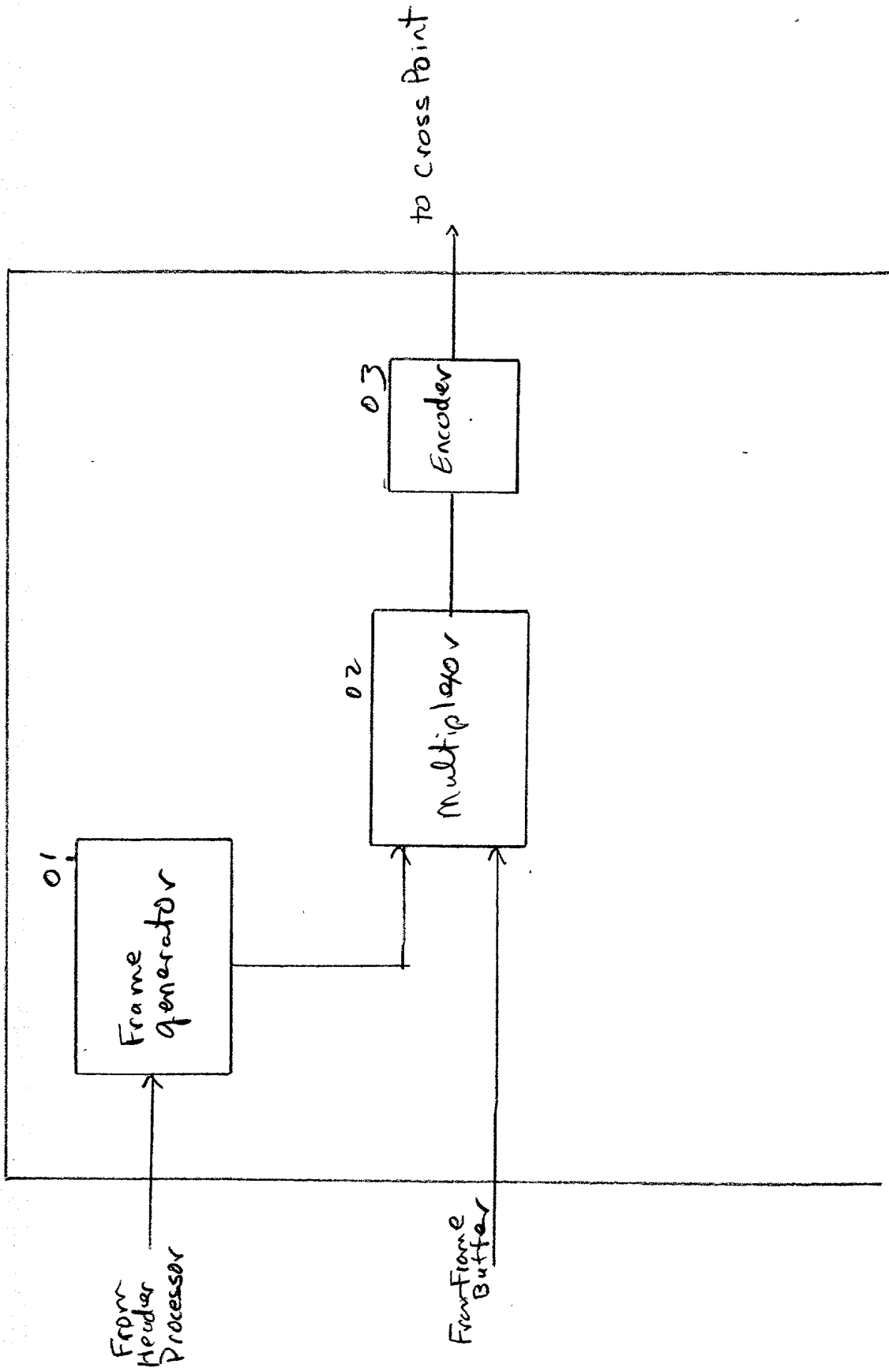


Fig 5



F.86

FIG. 40

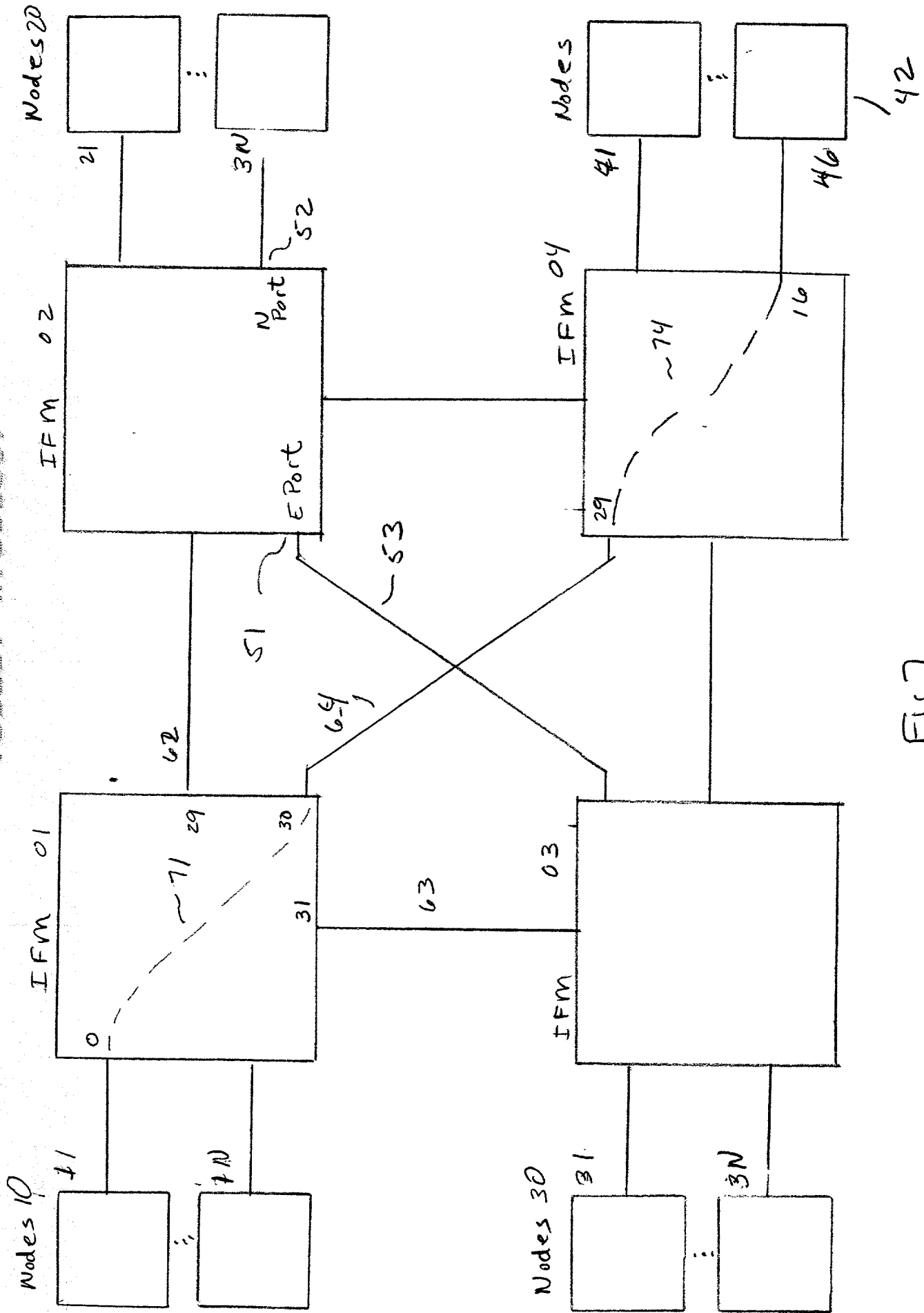


Fig 7





# Quad Switch Protocol Controller

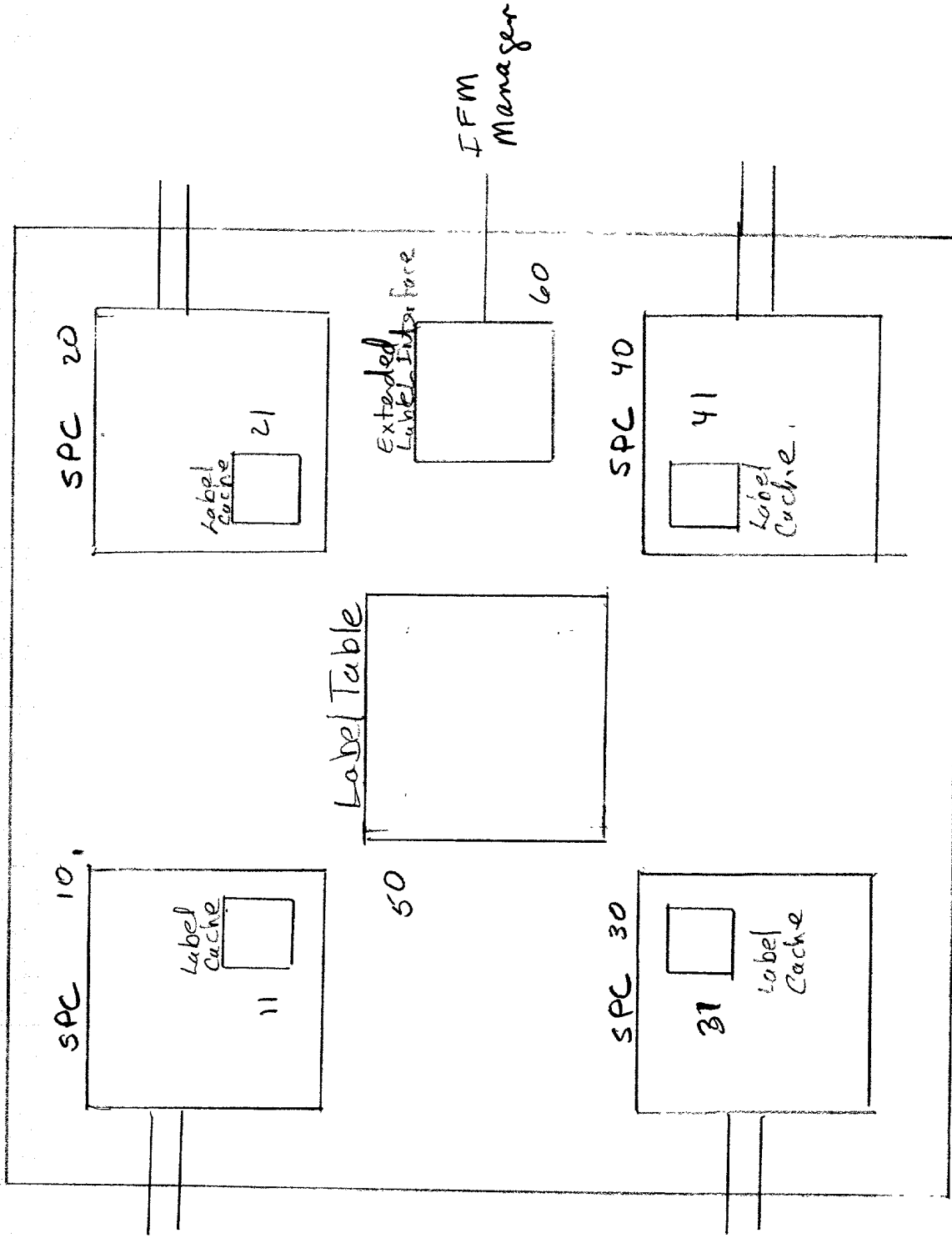


Fig 9



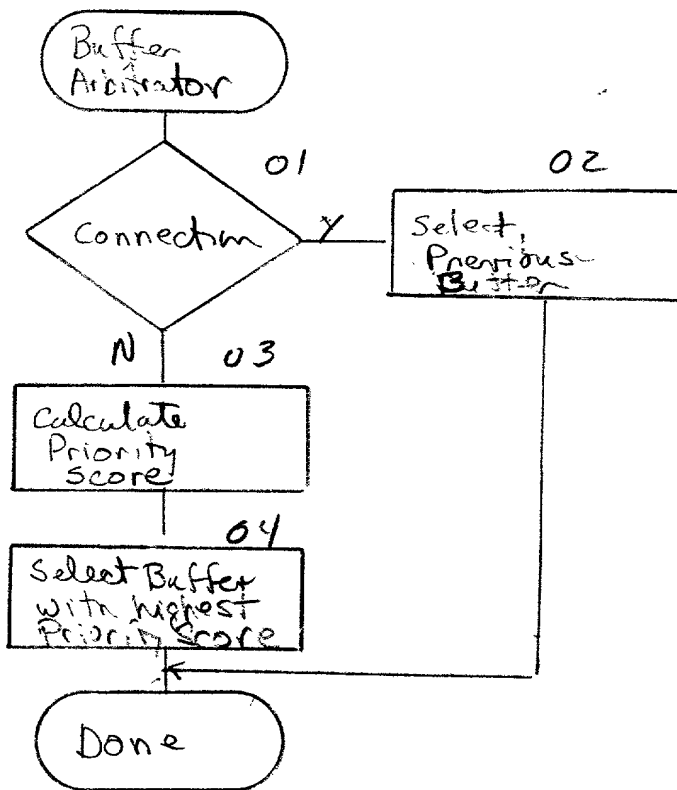


Fig 11

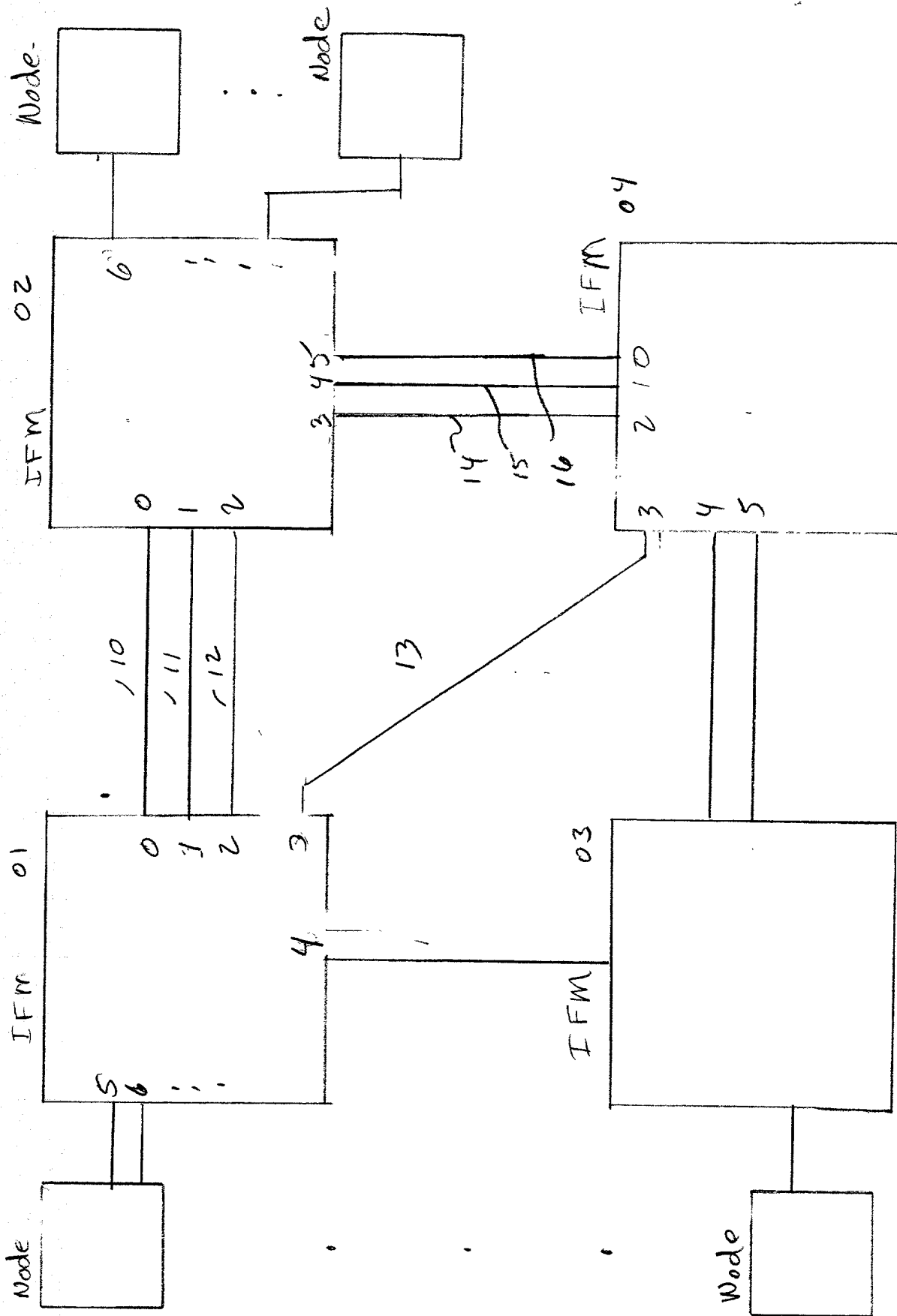


Fig 12

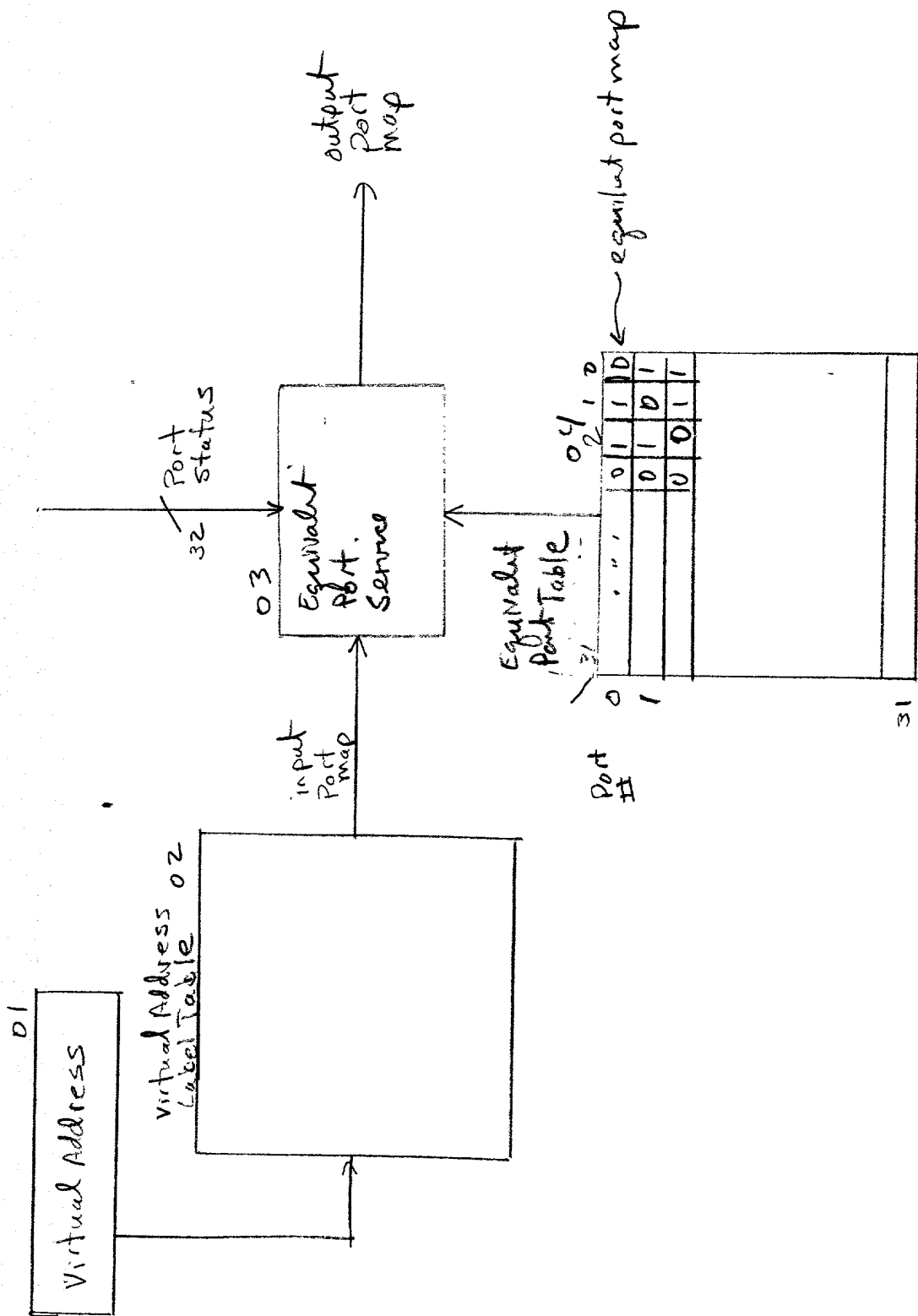


Fig 13

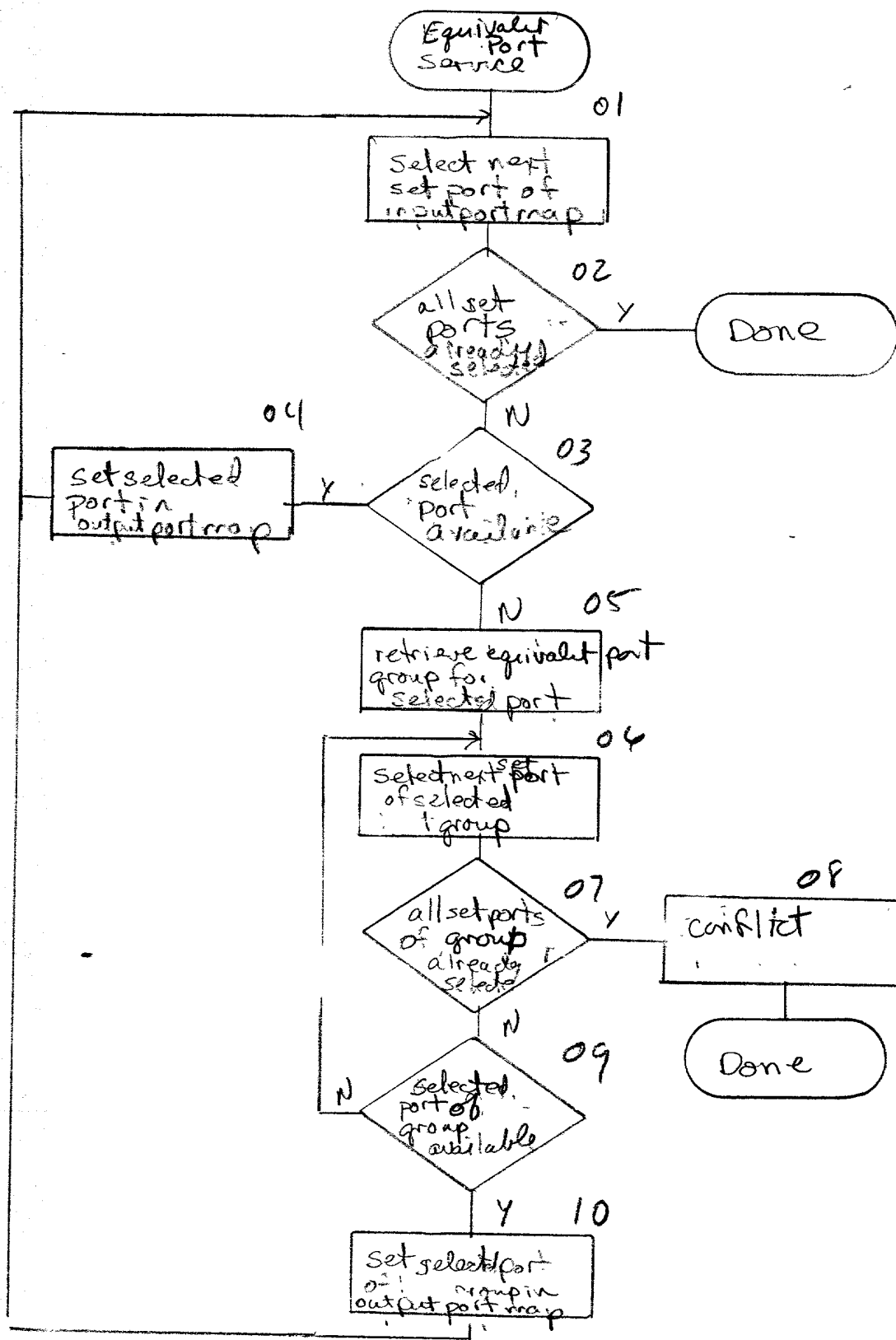


Fig 14

Header Processor

Destination Identifier

Virtual Address

01

any SPK

03

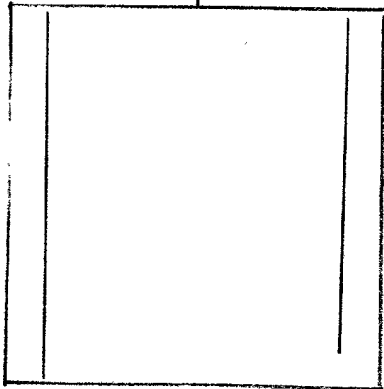
Comparator

enable port 32

enable port 33

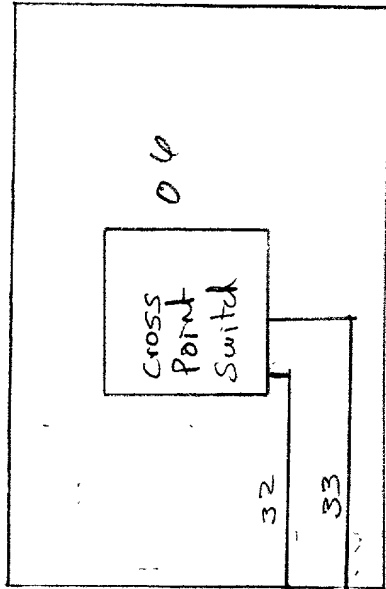
Reserved  
virtual Address Table

02



IFM

05



04

Cross  
Point  
Switch

32

33

IFM Manager

07

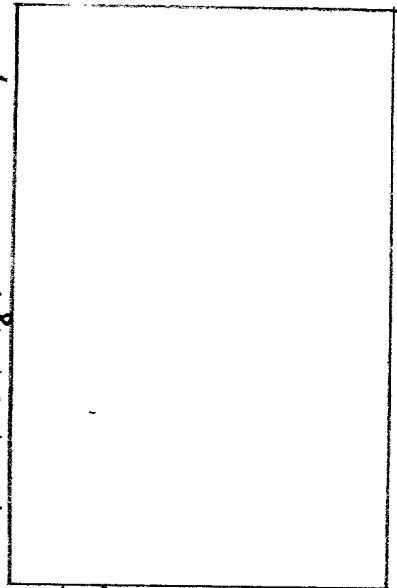


Fig 15

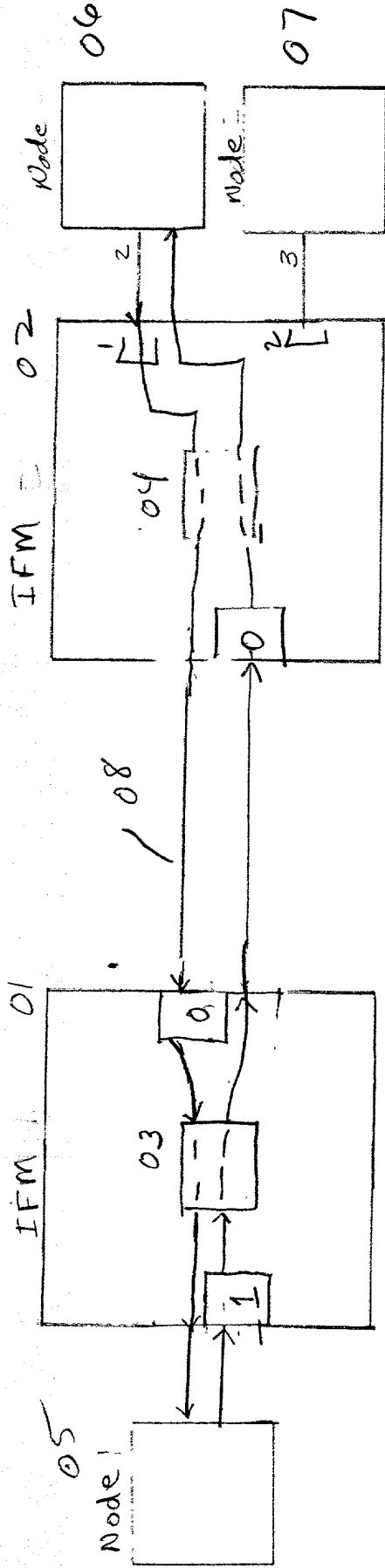


Fig 16

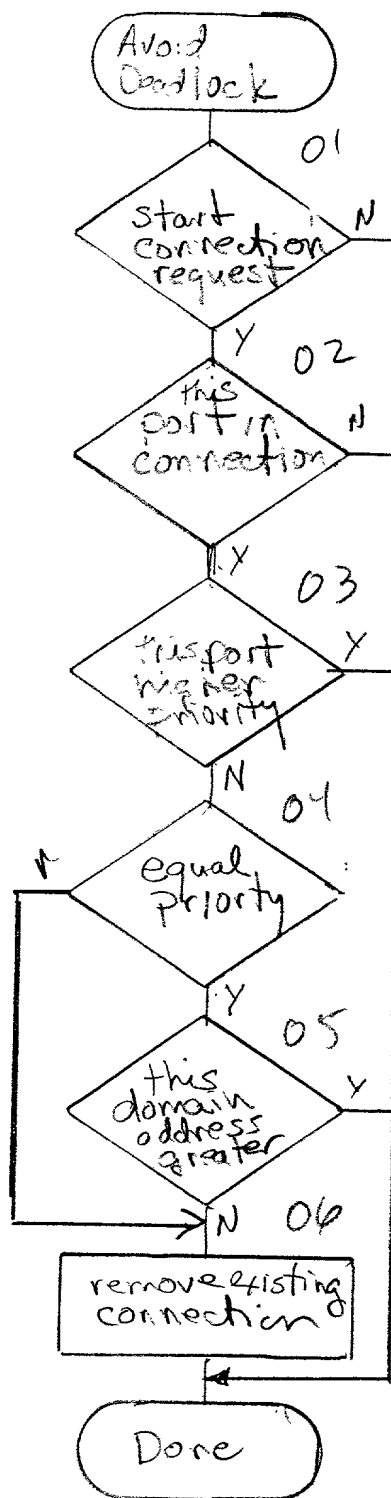
Deadlock

10

Time Node 1605 IFM 1601 Node 1606 IFM 1602  
 0 Send start connect 1 1 2 Send start connect 1 1 2

1	Connect 1 ↔ 0	Connect 2 ↔ 0
2	Forward start Connect 1	Forward start Connect 1
3	Send forward start connect Node 1	Can't forward start connect

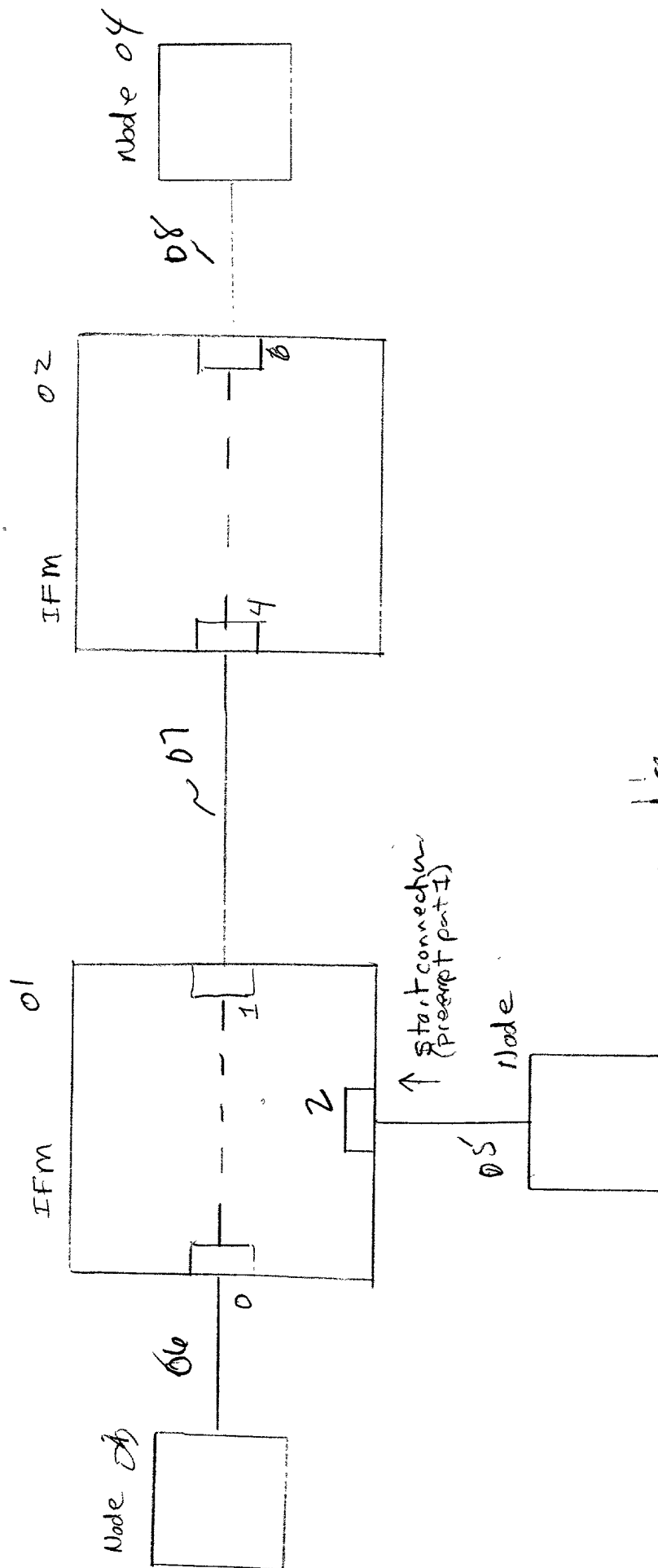




when end-to-end  
not established

Fig 17

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Fig

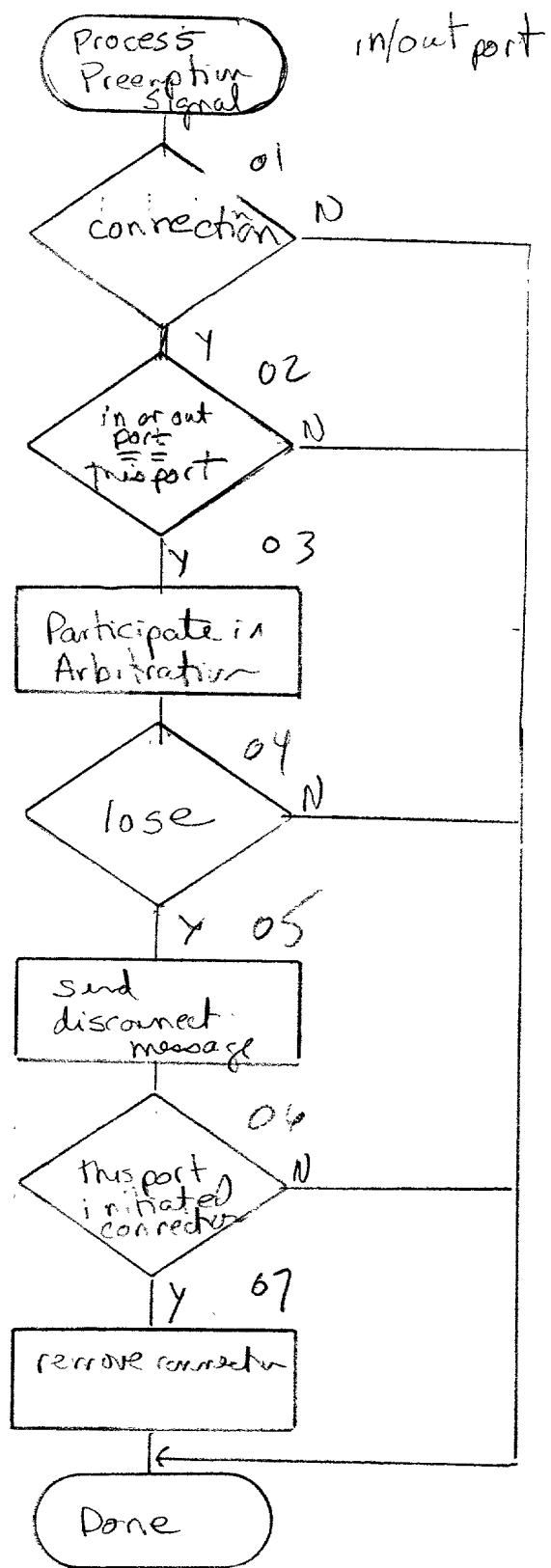


Fig. 19

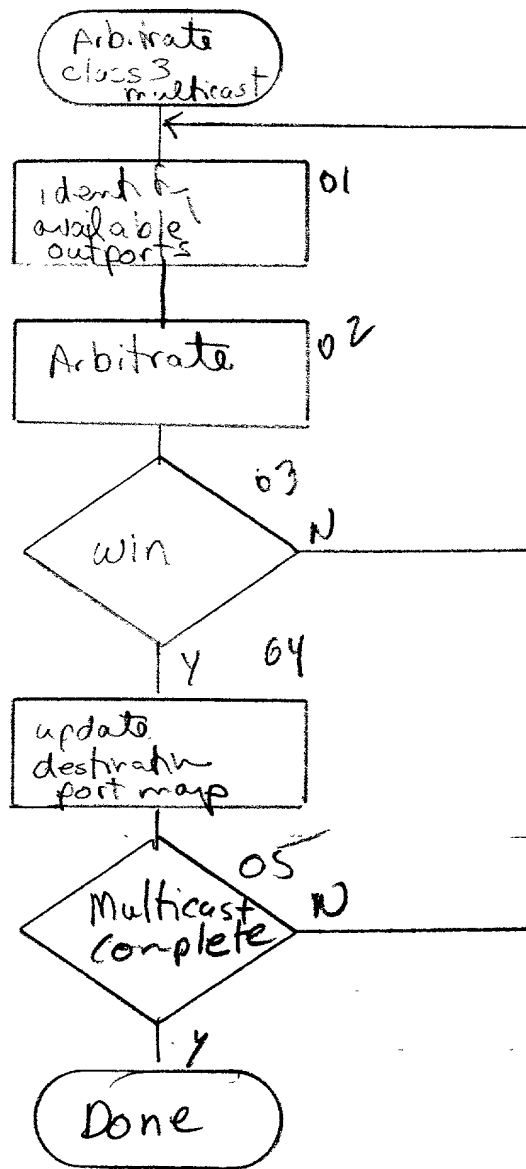


Figure 20